

## NEW ABSTRACT

A method and system for extrapolating a sampling rate from received digital cells in an integrated services hub in residential or business telecommunication systems. The system is implemented by a CPU controlling the hub operations. An input buffer is implemented by the CPU and its RAM. The CPU generates a fill level indicating the relative capacity of the buffer that is filled with samples not yet coupled to a CODEC. The CPU produces a divisor based on the fill level. A baud rate generator in the CPU uses the CPU local clock and the divisor to produce a sample rate at which buffer samples are coupled to the CODEC. The divisor is adjusted to maintain the fill level within a desired range. The sample rate is also used to sample analog signals coupled to the CODEC.